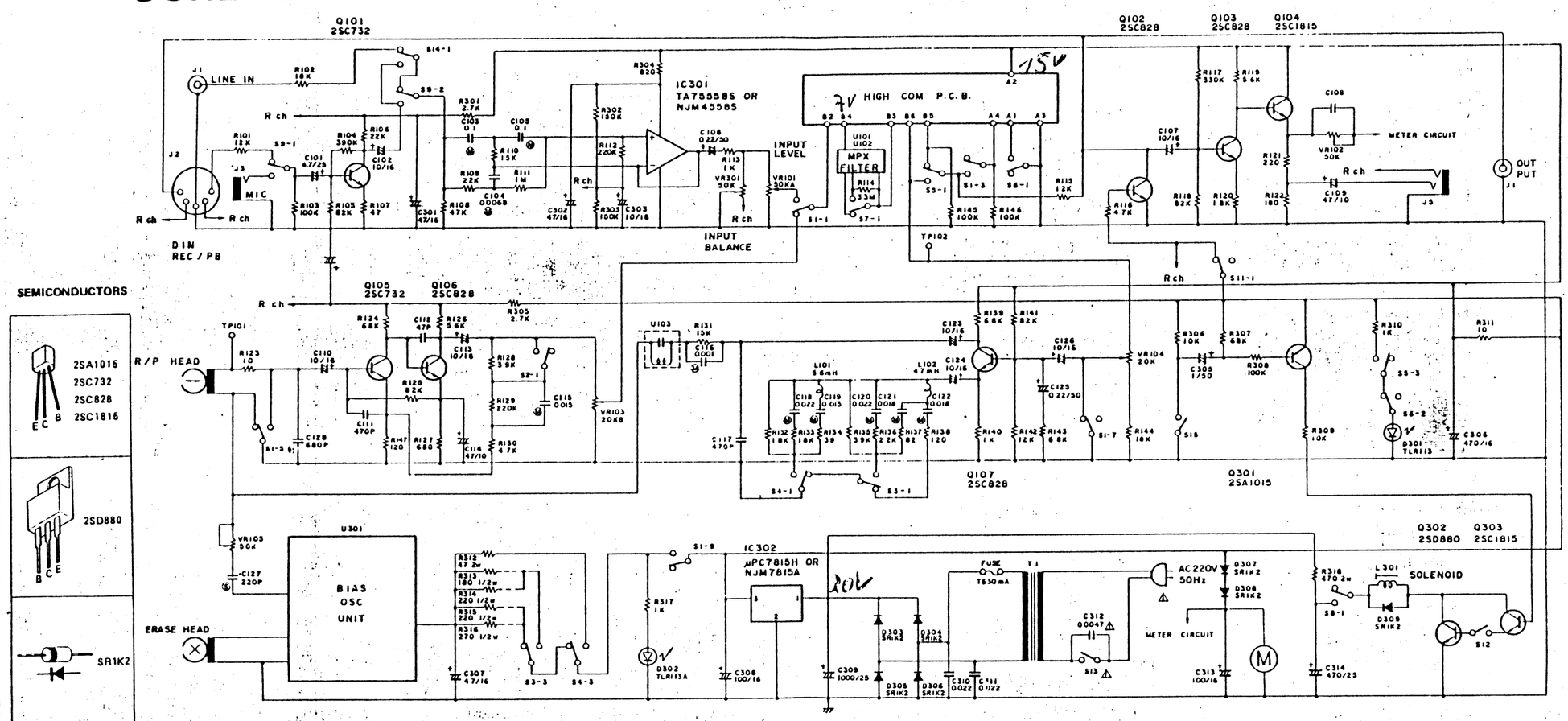
NIKKO

SCHEMATIC DIAGRAM

Nikko ND 500 it



SEMICONDUCTORS

SWITCH

S1-1-9	RECORD-PLAYBACK SW		(PLAY POSITION)
S2-1-2	NORMAL	TAPE SW	(NORMAL POSITION)
S3-1-4	CrO2		
S4-1-4	METAL		
S5-1-4	NR SW		(ON POSITION)
S6-1-2	HIGH COM SW		(HIGH COM POSITION)
S7-1-2	MPX SW		(OFF POSITION)
S8-1-2	MEMORY SW		(OFF POSITION)
S9-1-2	L ch	MIC SW	(OFF POSITION)
S10-1-2	R ch		
S11-1-2	MUTING SW		(OFF POSITION)
S12	REW SW		(OFF POSITION)
S13	POWER SW		(OFF POSITION)
S14-1-2	DIN SW		(OFF POSITION)
S15	COUNTER SW		(OFF POSITION)

NOTES:

1. SCHEMATIC IS SUBJECT TO CHANGE WITHOUT NOTICE.

UNLESS OTHERWISE SPECIFIED;

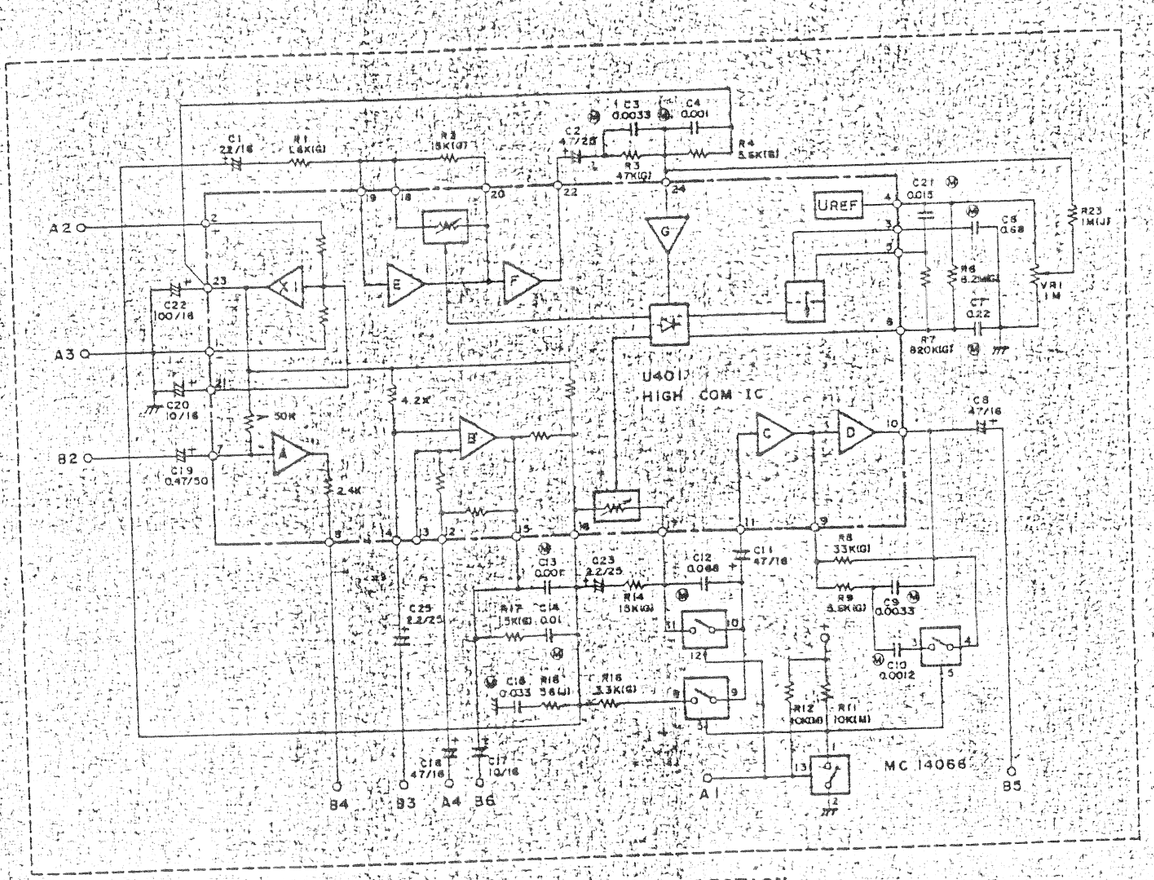
2. RESISTANCE VARUES ARE IN OHMS.
   K = 1,000 ; M = 1,000,000
3. CAPACITANCE VALUES 1.0 AND ABOVE ARE IN pF OR μF (P = pF, M = μF), LESS THAN 1.0 ARE IN μF. (ELECTROLYTIC CAPACITANCE VALUES ARE IN μF/WV.)

SCHEMATIC SYMBOLS:
Ⓜ POLYESTER FILM CAPACITOR
Ⓢ POLYSTYRENE FILM CAPACITOR

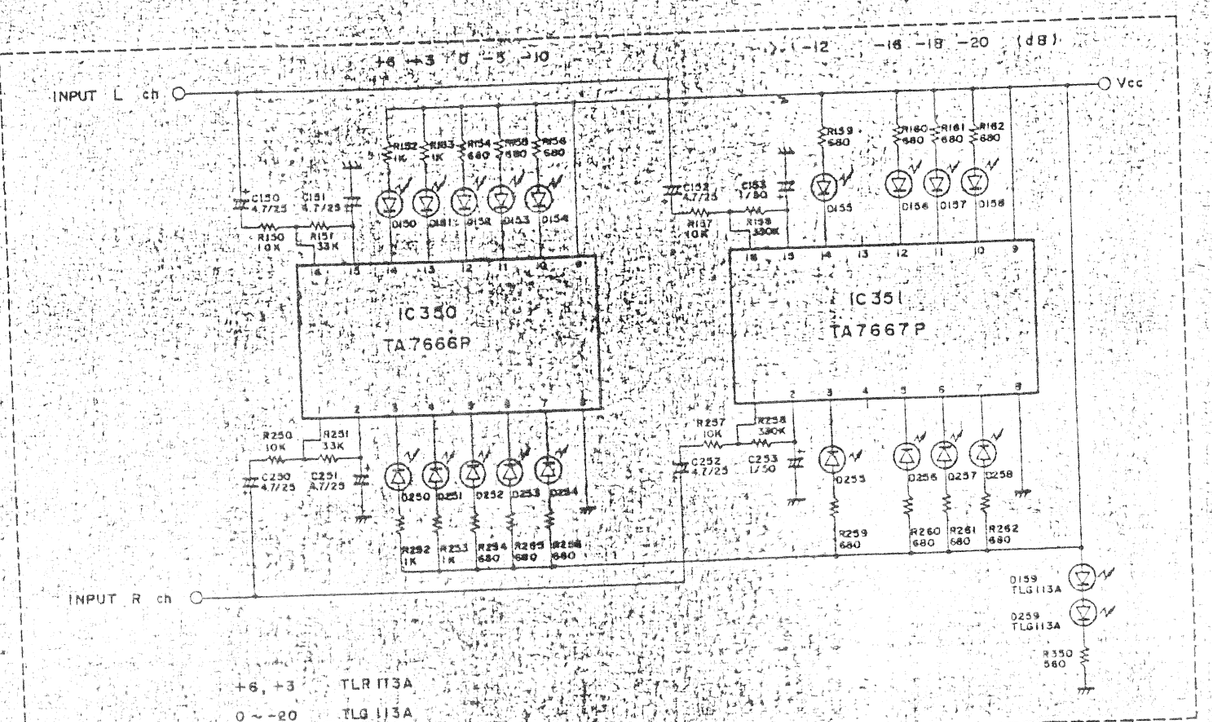
WARNING:

⚠ INDICATES SAFETY CRITICAL COMPONENTS. FOR CONTINUED SAFETY, REPLACE SAFETY CRITICAL COMPONENTS ONLY WITH MANUFACTURER'S RECOMMENDED PARTS.

B
Q1 6V
Q2 0V
Q3 7V
Q4 7V
Q5 0V
Q6 0V



HIGH COM P.C.B. SECTION



LED LEVEL DISPLAY SECTION